a video source capable of providing a digital YUV video signal;

a video output capable of connecting to a video display device; and

a digital processor that applies gamma correction to the digital YUV signal provided by

the video source and provides a corrected signal to the video output.

REMARKS

Applicant has carefully reviewed and considered the Office Action mailed on April 16, 2001, and the references cited therewith.

No claims are amended, cancelled, or added; as a result, claims 1-11 are now pending in this application.

§102 Rejection of the Claims

Claims 1-3, 5-8 and 10 were rejected under 35 USC § 102(e) as being anticipated by Aleksic et al. (U.S. 6,020,921). Anticipation requires the disclosure in a single prior art reference of each element of the claim under consideration. *In re Dillon* 919 F.2d 688, 16 USPQ 2d 1897, 1908 (Fed. Cir. 1990) (en banc), cert. denied, 500 U.S. 904 (1991). Applicant respectfully submits that the Office Action did not make out a *prima facie* case of anticipation, because the Aleksic reference does not disclose every element of the pending claims rejected here.

Aleksic discusses in col. 1, lines 37-39, and in column 2, lines 64-65, that a lookup table read-only memory (ROM) is required in a typical computer system to perform gamma correction of a YUV signal. Aleksic claims improvement on the read-only memory gamma correction system by implementing three circuits to perform straight-line approximation of a gamma correction curve, and switching between these three circuits based on a comparison circuit that makes a circuit selection determination based on the received input.

In contrast, the present invention utilizes and claims a digital processor employing a corrective algorithm that applies gamma correction to a digital YUV signal. That is, the present invention does not utilize a lookup table or switch between straight-line approximations of a gamma correction curve as are discussed in Aleksic, but instead utilizes a digital processor to computationally apply a corrective algorithm to the digital YUV signal to perform gamma

correction.

Because the cited reference fails to teach the element of the digital processor employing a corrective algorithm, applicant believes that claims 1 and 6, and the claims that depend therefrom, are in condition for allowance. Reexamination and allowance of these claims is therefore respectfully requested.

§103 Rejection of the Claims

Claims 4, 9, and 11 were rejected under 35 USC § 103(a) as being anticipated by the single reference Aleksic et al. (U.S. 6,020,921). Applicant respectfully traverses the single reference rejection under 35 U.S.C. § 103 since not all of the recited elements of the claims are found in Aleksic.

Specifically, the digital processor employing a corrective algorithm to perform gamma correction is not present in Aleksic, and differs significantly in structure and function from anything that is found in Aleksic as explained above. Further, as claims 4 and 9 depend from a base claim believed to be in condition for allowance as explained above, applicant believes that claims 4 and 9 are in condition for allowance as properly dependent upon an allowable base claim.

If the Examiner intends to take Official Notice of these missing elements, Applicant respectfully objects to the taking of Official Notice with a single reference obviousness rejection and, pursuant to M.P.E.P. § 2144.03, Applicant respectfully traverses the assertion of Official Notice and requests that the Examiner cite references in support of this position.

Because the claims here rejected have been shown to contain elements not present in the Aleksic reference, reexamination and allowance of these pending claims is respectfully requested.

Conclusion

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney (612-349-9581) to facilitate prosecution of this application.

RESPONSE UNDER 37 CFR § 1.111   **Page 5**
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If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 50-0439.

Respectfully submitted,

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Date _____ By _____
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                                    Reg. No. 44,639

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 24 day of July , 2001.

Candis B. Buending
Name   Signature